## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	("second limiting transistor" AND "second auxiliary transistor" AND "load path").CLM.	US-PGPUB; USPAT	OR	ON	2006/07/20 16:48
L2	0	("second limiting transistor" AND "second auxiliary transistor").CLM.	US-PGPUB; USPAT	OR	ON	2006/07/20 16:48
L3	2	("limiting transistor" AND " auxiliary transistor").CLM.	US-PGPUB; USPAT	OR	ON	2006/07/20 16:49

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